

ABSTRACT OF THE DISCLOSURE

A signal processing circuit outputs an output signal corresponding to a pulse width of an input pulse signal. This signal processing circuit comprises

5 integrating means for integrating pulse widths of the input pulse signal for a predetermined period of time, and outputting means for outputting the output signal corresponding to the pulse widths integrated by the integrating means. Each of these pulse widths has one of

10 positive and negative polarities.

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